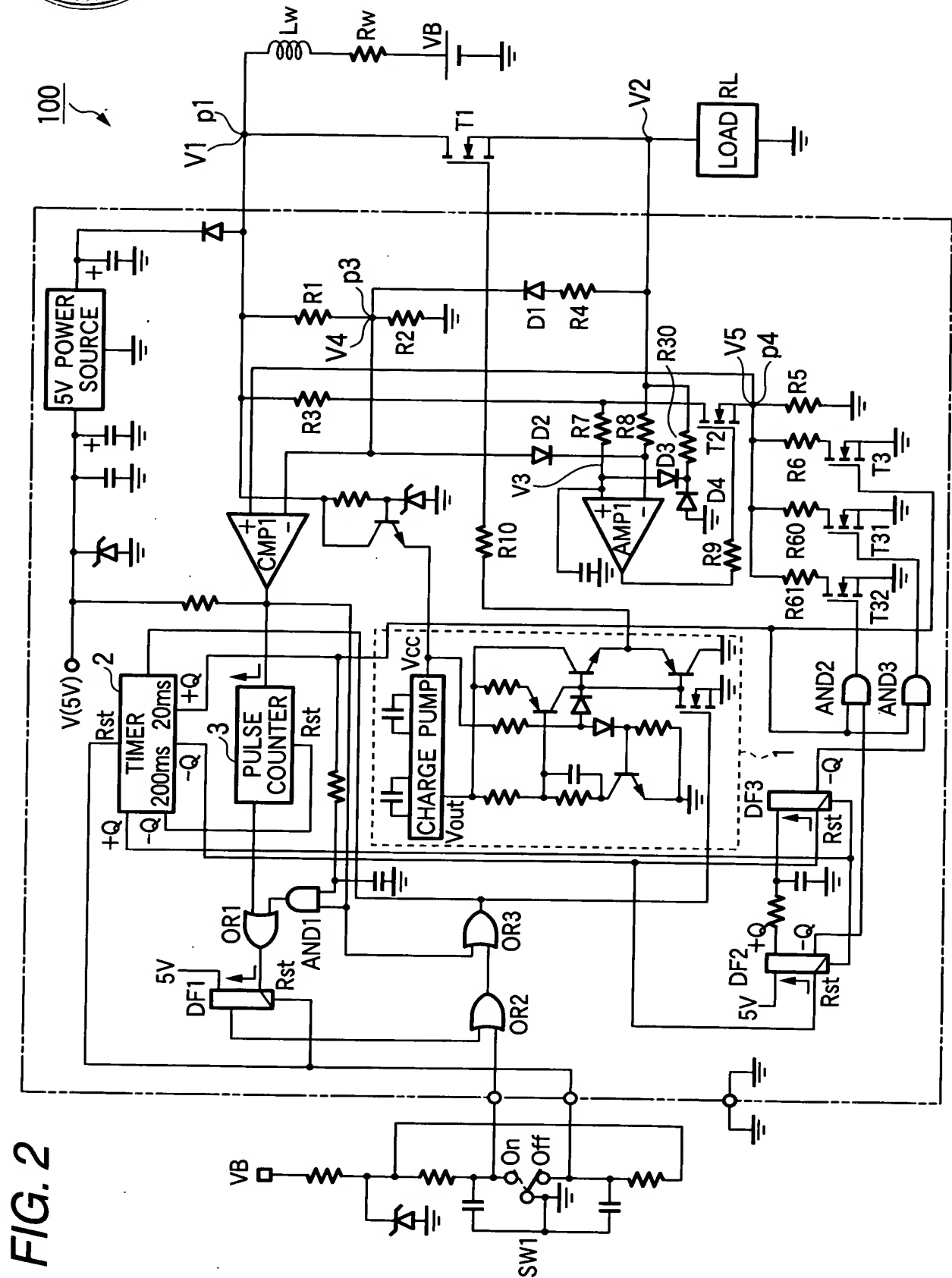


The diagram shows a Class-AB push-pull amplifier circuit. It includes two operational amplifiers: a comparator (CMP1) and a main amplifier (AMP1). The feedback loop consists of a resistor network (R1, R2, R3, R4, R7, R8, R9, R10) and a diode (D1). The output stage uses two transistors, T1 (NPN) and T2 (PNP), to drive a load (LOAD RL). The circuit is powered by a supply voltage (VB) and includes a load resistor (Rw) and an inductor (Lw) in the feedback path. The output voltage is labeled V1, and the load voltage is labeled V2. The input signal is applied to the non-inverting input of the comparator (CMP1).





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FIG. 3

